

# Breaking and Fixing Critical Infrastructure

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# What is Critical Infrastructure?



Assets that are essential for the functioning of a society and economy

- Food and Agriculture
- Banking and Finance
- Chemical
- Commercial Facilities
- Communications
- Critical Manufacturing
- Dams
- Defense Industrial Base
- Emergency Services
- Energy
- Government Facilities
- Healthcare and Public Health
- Information Technology
- National Monuments and Icons
- Nuclear Reactors, Materials and Waste
- Postal and Shipping
- Transportation Systems
- Water

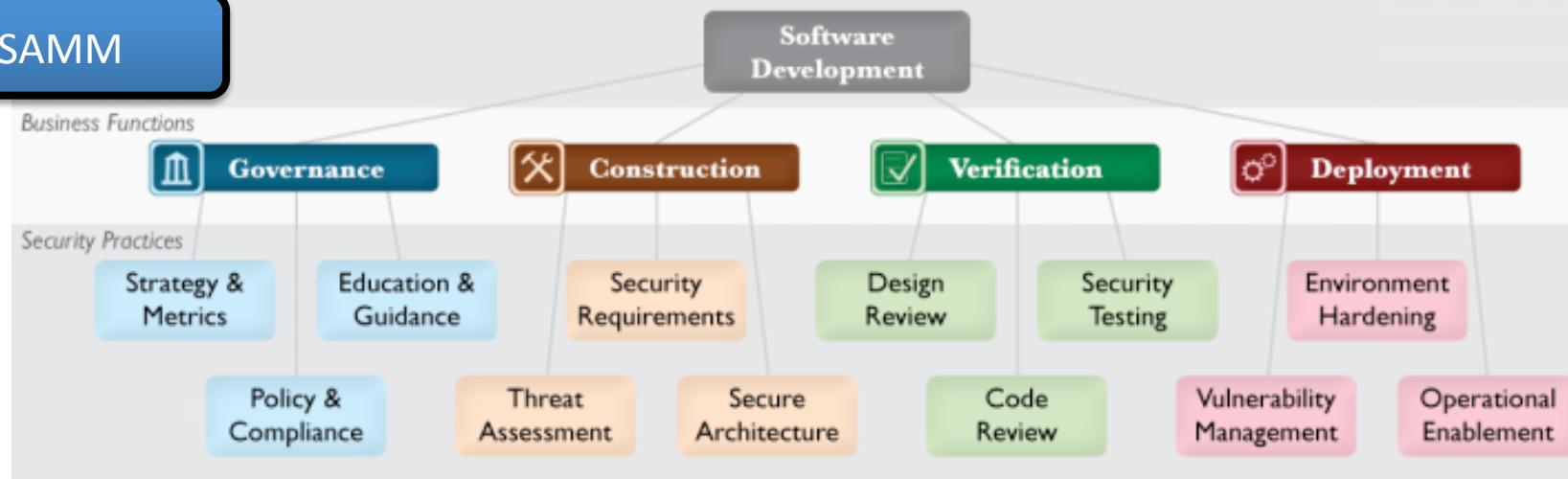
# Traditional Security Models

## The Software Security Framework (SSF)

BSIMM

Governance	Intelligence	SSDL Touchpoints	Deployment
Strategy and Metrics	Attack Models	Architecture Analysis	Penetration Testing
Compliance and Policy	Security Features and Design	Code Review	Software Environment
Training	Standards and Requirements	Security Testing	Configuration Management and Vulnerability Management

OpenSAMM



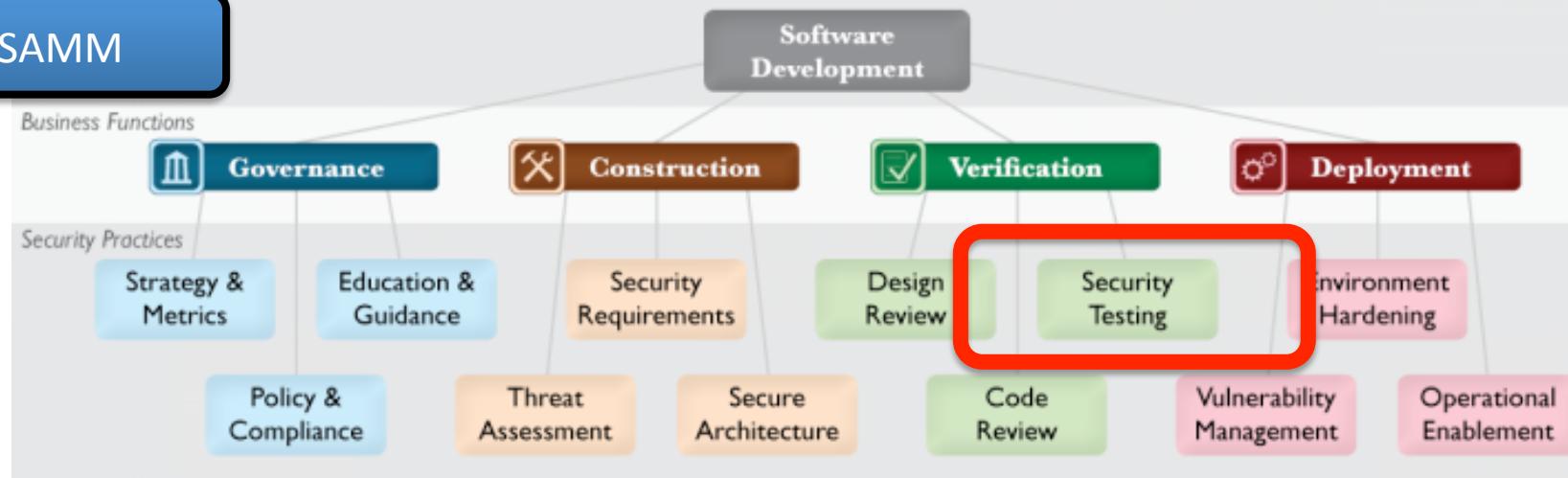
# What Does These Mean?

## The Software Security Framework (SSF)

BSIMM

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OpenSAMM

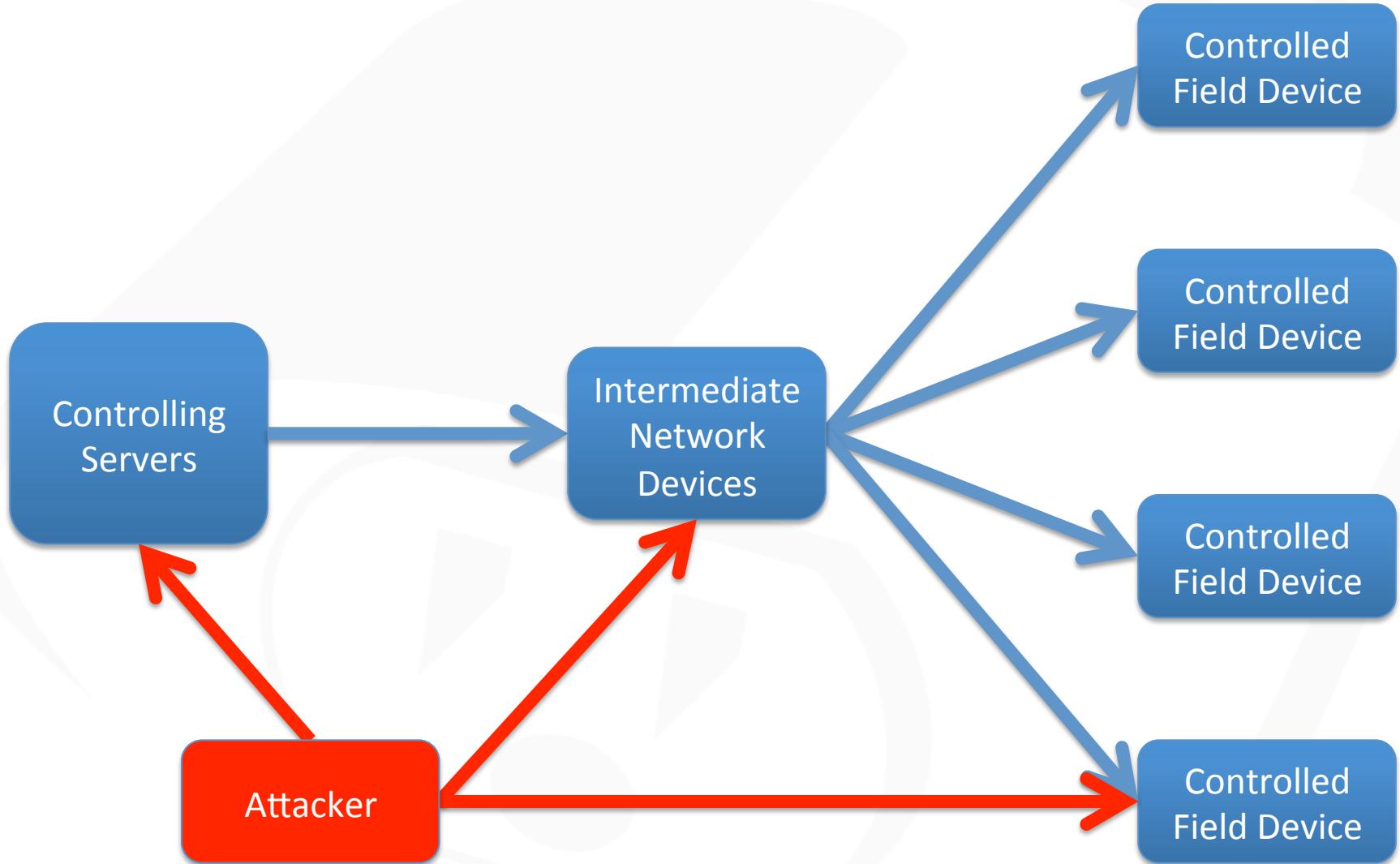


# Breaking In Before Attackers Do

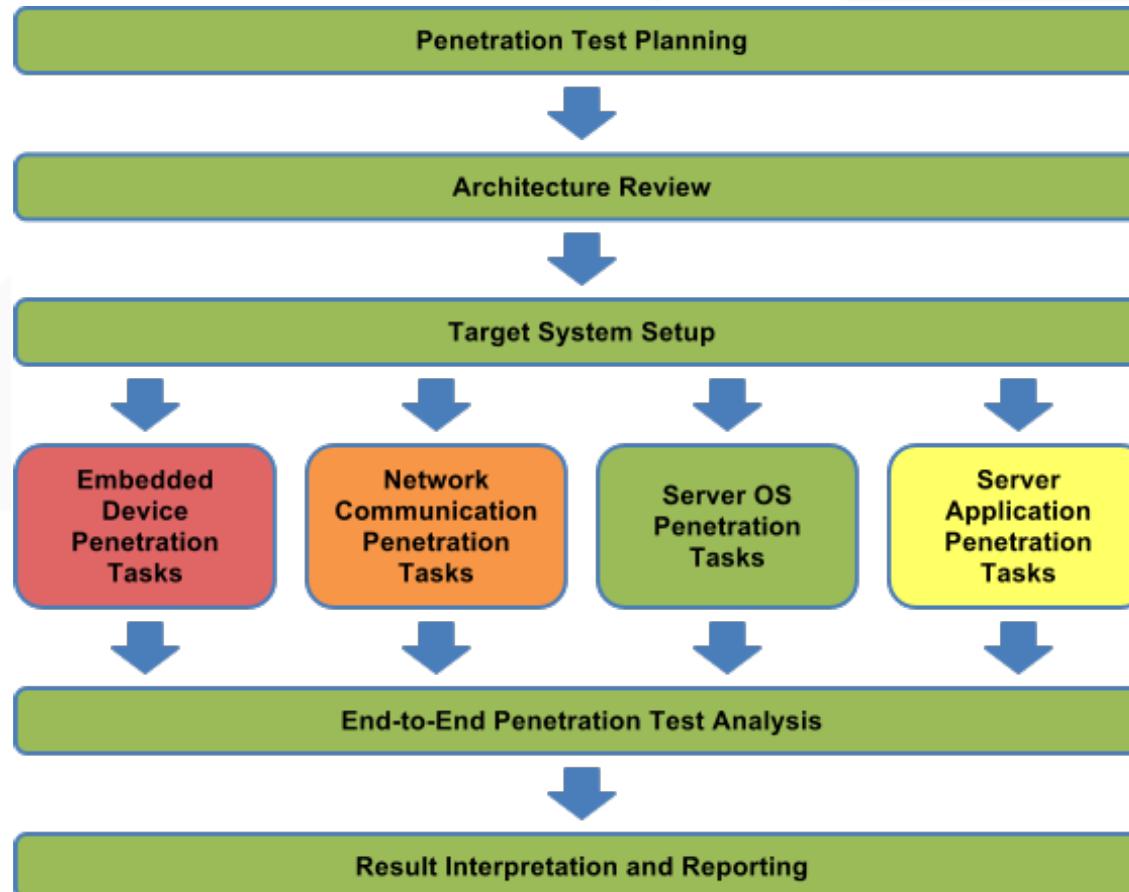


- Penetration testing is the MOST underutilized process in these models by most companies across the world!
- Most money and time is spent on governance, awareness, architecture, and defense
  - Most companies may check to see if there their front door is locked, but rarely check to see if they have more than one door, or where those extra doors lead...
  - And companies are always surprised when they are compromised...
- Continuous, agile penetration testing helps to address this
  - This should go far beyond vulnerability scanning, include exploitation
  - Hire or train at least one competent employee
  - Set this as their sole priority, testing both the outside perimeter and inside network segments, with targets changing at least weekly
  - Have them find and follow the paths of least resistance like attackers do
  - Hand off vulnerability remediation and tracking to a different individual

# Critical Infrastructure Control



# Penetration Testing Methodology



- Green: Tasks most frequently and require the most basic of penetration testing skill
- Yellow: Tasks commonly performed and require moderate penetration testing skill
- Orange: Tasks that are occasionally performed but require higher levels of expertise
- Red: Tasks performed infrequently and require highly specialized skills

# Can Attackers Reach Field Assets?



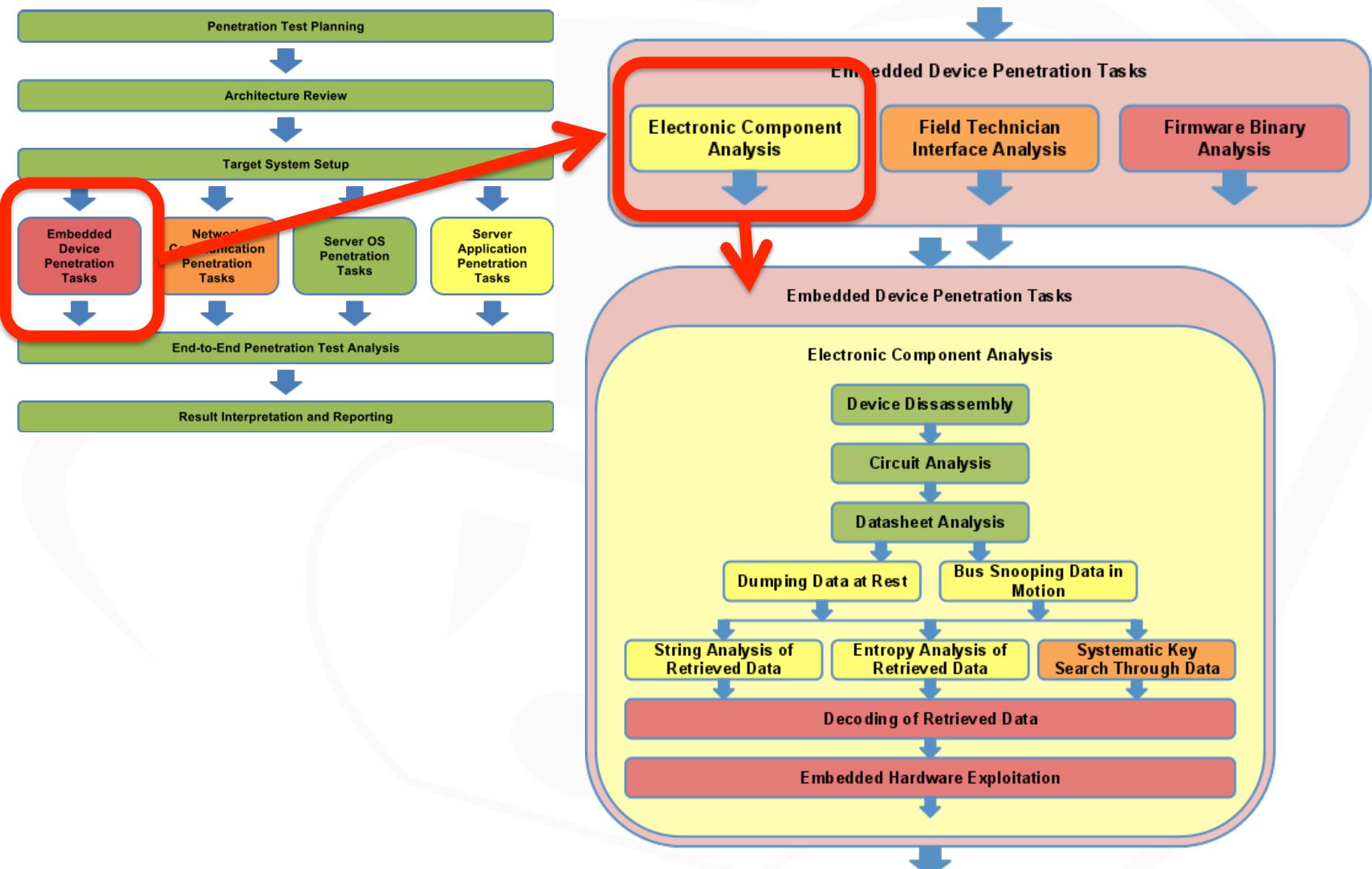
- Many field deployed Critical Infrastructure devices have tamper detection and tamper protection mechanisms
- Locked cases and enclosures
  - usually trivial to pick
- Tilt and vibration detection components
  - can often be disabled before triggering once you locate them
- Cameras
  - often not monitored
  - long response times if they trigger

# What To Do With Field Devices?



- Attacking data at rest
  - Power down the device, expose its circuit board, and interact directly with each component
  - Extract contents of accessible RAM, Flash, and EEPROM
  - Identify cryptography keys or firmware
- Attacking data in motion
  - Boot and normally operate the device in a lab, monitoring bus activity between major chips (MCU, Radio, Flash, RAM)
  - Crypto keys can often be found in key load operations between a microcontroller and crypto accelerator
  - Firmware can often be found in boot processes (between Flash and MCU) and firmware updates (between Radio, MCU, and Flash)

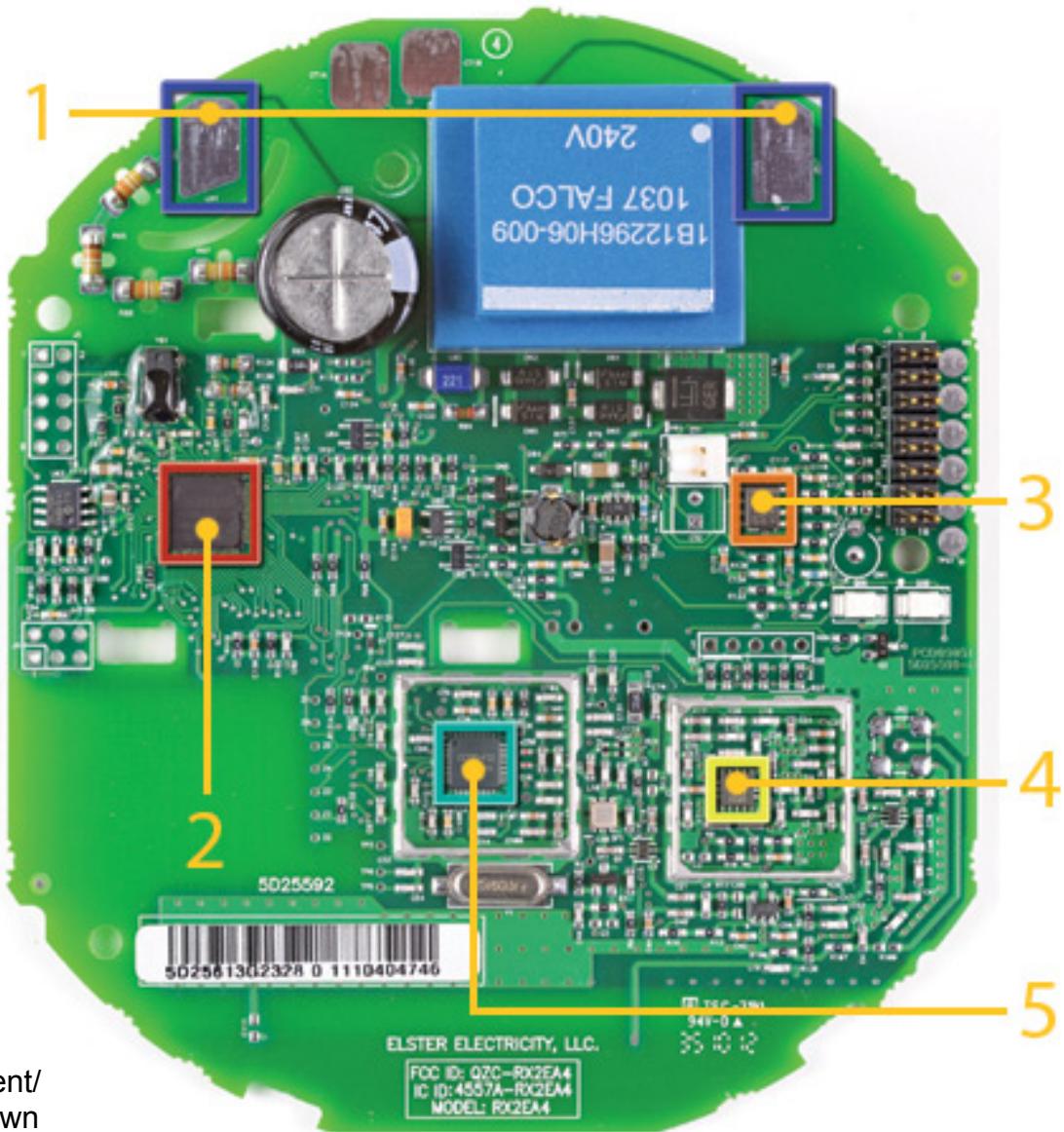
# Embedded Device Pentest Tasks



# Embedded Field Device Disassembled

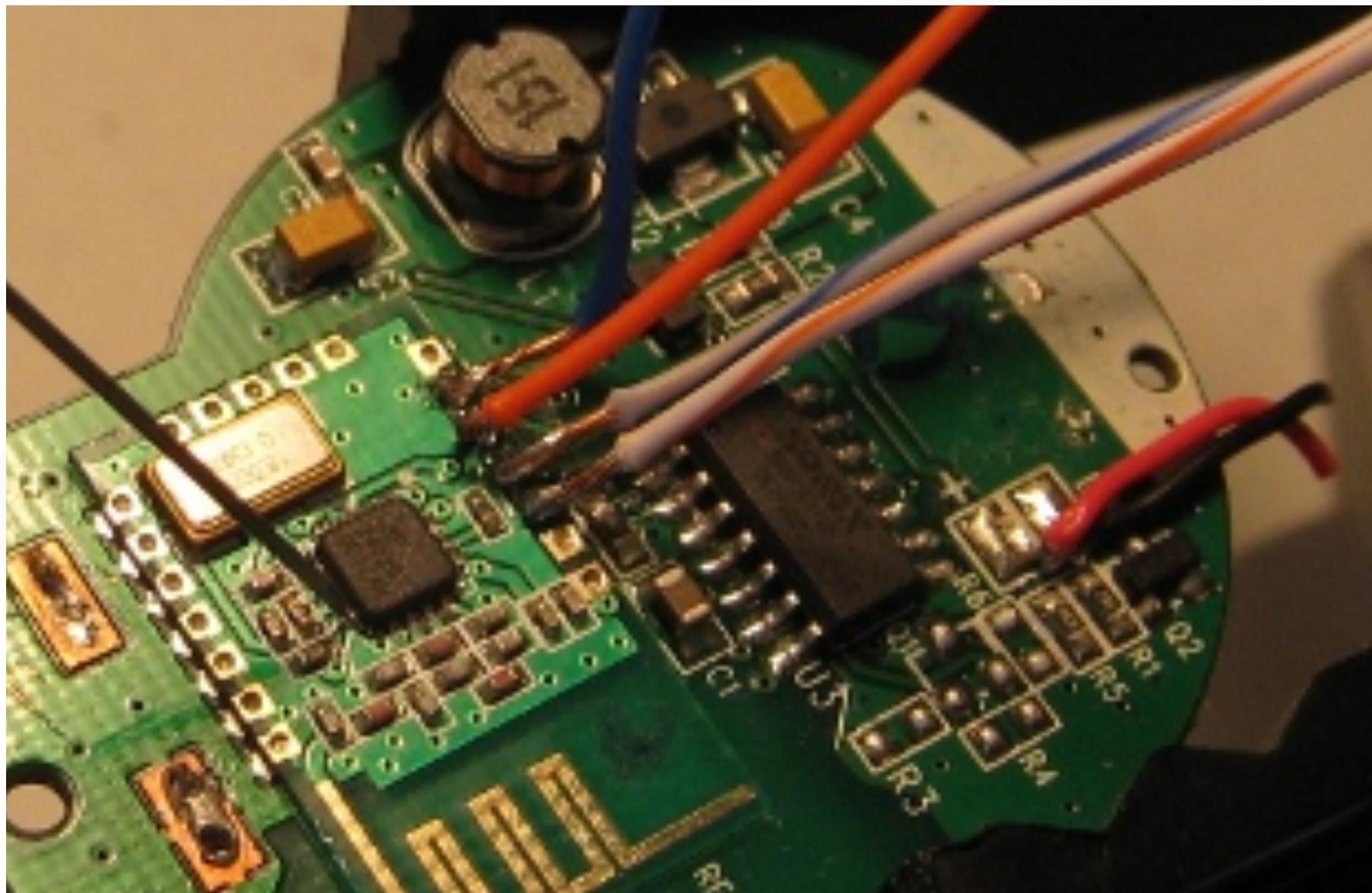


1. 240v Connections
2. Microcontroller  
(Teridian 71M6531F SOC)
3. Dual Operational Amplifier  
(LM2904)
4. ISM Band RF Amplifier  
(RFMD RF2172)
5. ISM Band RF  
(TI CC1110F32)



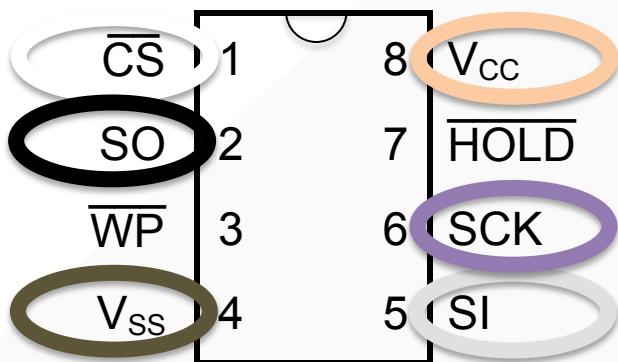
Source - <http://www.edn.com/design/power-management/4368353/What-s-inside-a-smart-meter-iFixit-tears-it-down>

# Embedded Device Testing

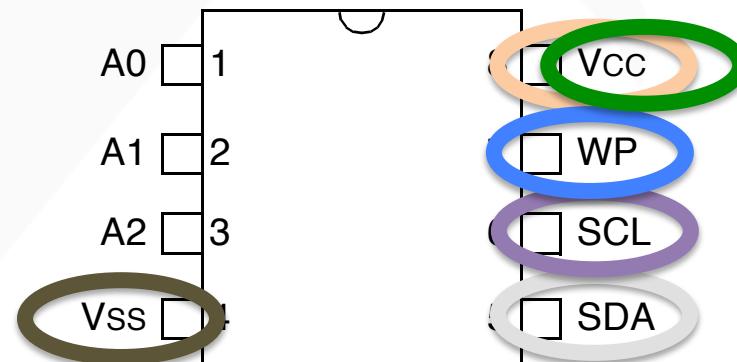


# Bus Pirate to EEPROMs

CAT25080 – SPI

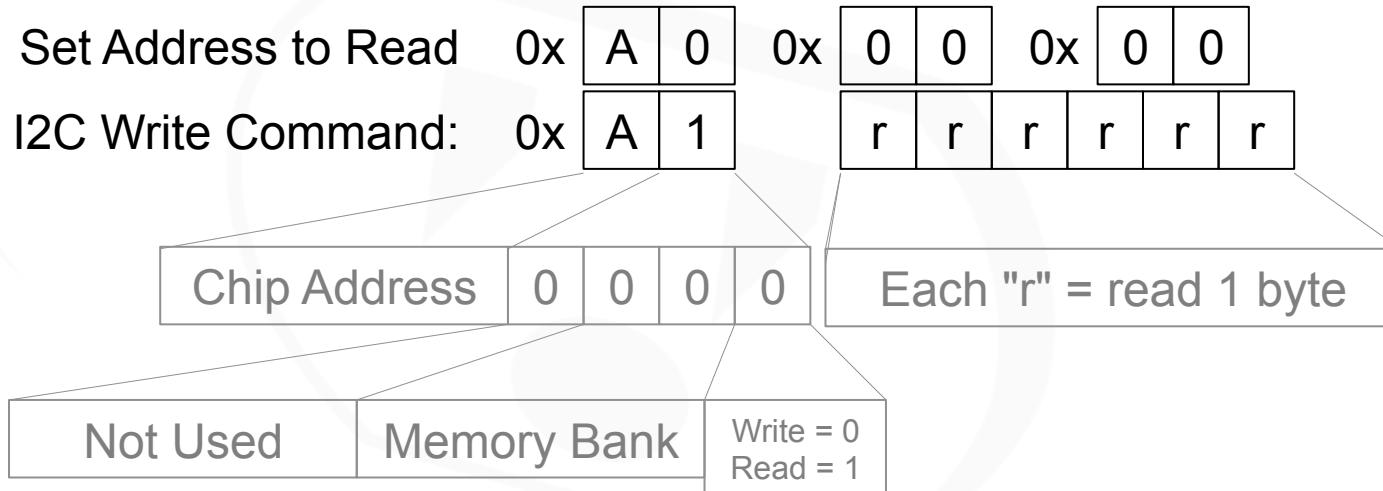
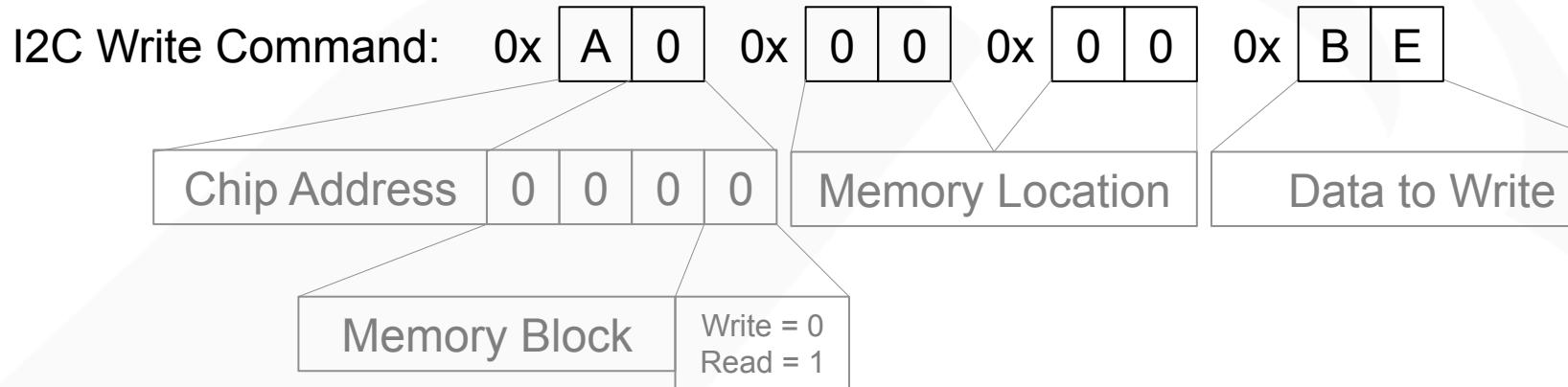


24LC088 – I2C



Mode	MOSI	CLK	MISO	CS
HIZ				
1-Wire	OWD			
UART	TX		RX	
I2C	SDA	SCL		
SPI	MOSI	CLOCK	MISO	CS
JTAG	TDI	TCK	TDO	TMS

# I2C Protocol



# Dumping EEPROMs and Flash

**Aardvark I2C/SPI Control Center**

### I2C Control

Bitrate: Set 400 kHz

**Master** Slave

Slave Addr: 50 (For Hex: enter "0x....")

Features:  10-Bit Addr  Combined FMT  No Stop

Master Write

Message  
00 00

Master Write

**Clear** **Load** **Save**

Master Read

Number of Bytes: 64

Master Read

**Clear** **Load** **Save**

### SPI Control

Bitrate: Set 1000 kHz

Polarity:  Rising/Falling  Falling/Rising

Phase:  Sample/Setup  Setup/Sample

Bit Order:  MSB  LSB

**Master** Slave

SS Polarity:  SS Active Low  SS Active High

MOSI Message

```
03 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
```

**Send**

**Transaction Log**

me	Mod.	R/W	M/S	Feat.	B.R.	Addr.	Length	Data
09-10-30 11:55:18.119	I2C							I2C Pullups Enabled
09-10-30 11:55:18.145	I2C							I2C Bitrate Set to: 100
09-10-30 11:55:18.172	I2C	W	M	--S	100	0x50	1	00
09-10-30 11:55:18.228	I2C	R	M	---	100	0x50	256	00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E
09-10-30 11:56:27.917	I2C	W	M	---	400	0x32	0	
09-10-30 11:56:29.966	I2C	R	M	---	400	0x32	0	
09-10-30 11:56:42.149	I2C	W	M	---	400	0x32	0	

**Clear Log** **Save to File**

# Task: Bus Snooping Data in Motion



spi-eeprom - Total Phase Data Center  
22.77 KB

Index	m:s.ms.us	Dur	Len	Err	Record	Data
0	0:00.000.000				Capture started [Wed May 13 16:19:19 2009]	
1	0:04.581.968	31.8 us	1 B		Transaction	0600
4	0:04.585.123	562 us	35 B		Transaction	0200 0000 0000 0000 0100 0200 0300 0400 0500 0600 0700 0800 ...
7	0:04.597.103	31.8 us	1 B		Transaction	0600
10	0:04.600.128	562 us	35 B		Transaction	0200 0000 2000 2000 2100 2200 2300 2400 2500 2600 2700 2800 ...
13	0:04.611.834	31.8 us	1 B		Transaction	0600
16	0:04.613.939	562 us	35 B		Transaction	0200 0000 4000 4000 4100 4200 4300 4400 4500 4600 4700 4800 ...
19	0:04.627.824	31.8 us	1 B		Transaction	0600
22	0:04.629.945	562 us	35 B		Transaction	0200 0000 6000 6000 6100 6200 6300 6400 6500 6600 6700 6800 ...
25	0:04.643.797	31.8 us	1 B		Transaction	0600
28	0:04.645.951	562 us	35 B		Transaction	0200 0000 8000 8000 8100 8200 8300 8400 8500 8600 8700 8800 ...
31	0:04.659.980	31.9 us	1 B		Transaction	0600
34	0:04.663.135	562 us	35 B		Transaction	0200 0000 A000 A000 A100 A200 A300 A400 A500 A600 A700 A800 ...
37	0:04.674.856	31.8 us	1 B		Transaction	0600
40	0:04.676.994	563 us	35 B		Transaction	0200 0000 C000 C000 C100 C200 C300 C400 C500 C600 C700 C800 ...
43	0:04.690.846	31.8 us	1 B		Transaction	0600
46	0:04.693.032	563 us	35 B		Transaction	0200 0000 E000 E000 E100 E200 E300 E400 E500 E600 E700 E800 ...
49	0:07.525.877				Capture stopped [Wed May 13 16:19:27 2009]	
50	0:00.000.000				Capture started [Wed May 13 16:19:28 2009]	
51	0:02.722.080	1.06 ms	67 B		Transaction	0300 0000 0000 0000 0001 0002 0003 0004 0005 0006 0007 0008 ...
54	0:05.012.317	1.06 ms	67 B		Transaction	0300 0000 4000 0040 0041 0042 0043 0044 0045 0046 0047 0048 ...
57	0:06.744.490	1.06 ms	67 B		Transaction	03FF 00FF 80FF 0080 0081 0082 0083 0084 0085 0086 0087 0088 ...
60	0:09.080.727	1.06 ms	67 B		Transaction	03FF 00FF C0FF 00C0 00C1 00C2 00C3 00C4 00C5 00C6 00C7 00C8 ...
63	0:11.318.410				Capture stopped [Wed May 13 16:19:39 2009]	

Search No filter: 64 records.

Protocol Lens: SPI

Command Line

```
Action cancelled.
> example
3> open('Applications/Data Center.app/example/spi-eeprom.tdc')
Buffer cleared.
File opened.
4> lens('spi')
Filter disabled.
Lens has been set to spi.
```

Details

Offset	0	1	2	3	4	5	6	7	ASCII
00000	02	00	00	00	01	02	03	04	.....
00008	05	06	07	08	09	0A	0B	0C	.....
00010	00	0E	0F	10	11	12	13	14	.....
00018	15	16	17	18	19	1A	1B	1C	.....
00020	1D	1E	1F						...
00028									
00030									
00038									

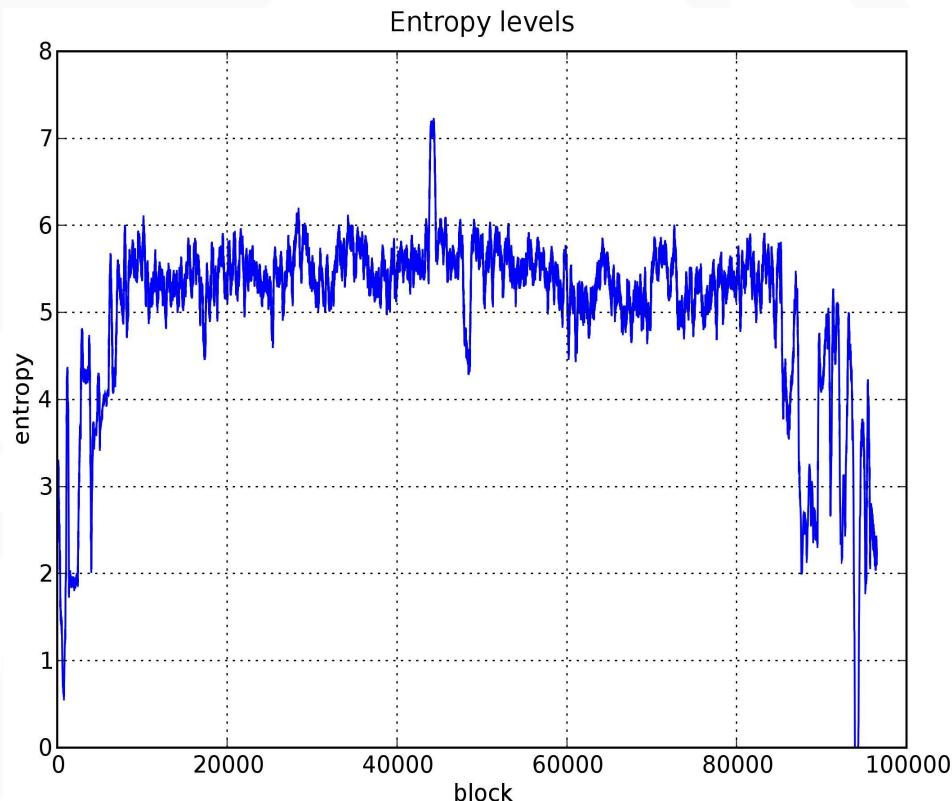
MOSI Data MISO Data Timing

Bus Filter Info

Ready Disconnected EN

# Using Entropy to Find Keys

- Asymmetric keys have high entropy (very random)
- RAM and Flash is filled with non-random data
- Graphing entropy of flash reveals a spike in randomness
- This spike is the location of the asymmetric key in flash



# Systematic Crypto Key Search



- Perform basic string searches for obvious keys
- Develop custom tools to do more advanced searches:
  - GoodFET: Abuses vulnerability in TI, Ember radios to access RAM even when chip is locked
  - zbgoodfind: Search for ZigBee key using RAM dump as a list of potential keys
  - Combined they can recover the ZigBee network key

```
$ sudo goodfet.cc dumpdata memdump.hex
Target identifies as CC2430/r04.
Dumping data from e000 to ffff as chipcon-2430-mem.hex.
...
$ objcopy -I ihex -O binary memdump.hex memdump.bin
$ zbgoodfind -R 802154_encr_sample.dcf -f memdump.bin
zbgoodfind: searching the contents of 802154_encr_sample.dcf for
encryption keys with the first encrypted packet in memdump.bin.
Key found after 6264 guesses:  c0 c1 c2 c3 c4 c5 c6 c7 c8 c9 ca cb cc
cd ce cf
```

# Termineter & John Commor



- Open a terminal to the "john\_common\_c1218" folder and start the serial emulation script

```
sudo ./pts_ports.sh
```

- Open a new terminal and start the virtual smart meter replacing the "???" with the port numbers displayed on the last command

```
sudo python john_common_c1218.py -e ??? -t ???
```

- Open a new terminal and use Termineter to read the first c12.22 table on the meter. Replace the "???" with the port you typed after the "-t" in the previous command

```
sudo ./termineter2.py
set connection /dev/pts/???
connect
use read_table
set tableid 1
run
```

- Now use Termineter to read tables 2 and 3



# Decompiled Code

**Memory: 0x080497f7**

Memory Expression: 0x080497f7 Memory Size: 1024 viv

```

20004b07, (1, 0, 234881040, 'data processing immediate shift', 8192, 0)
    5384: \x07\x4b\x00\x00

4b17681a, (1, 167772160, 234881040)
    5388: \x1a\x68\x17\x00

d005429a, (1, 16, 234881040, 'data processing immediate shift', 8192, 0)
    538c: \x9a\x42\x05\x00

681a4b0c, (1, 134217728, 234881040)
    5390: \x0c\x4b\x1a\x00

19b2380, (1, 0, 234881040, 'data processing immediate shift', 8192, 0)
    5394: \x80\x23\x9b\x00

d100429a, (1, 16, 234881040, 'data processing immediate shift', 8192, 0)
    5398: \x9a\x42\x00\x00

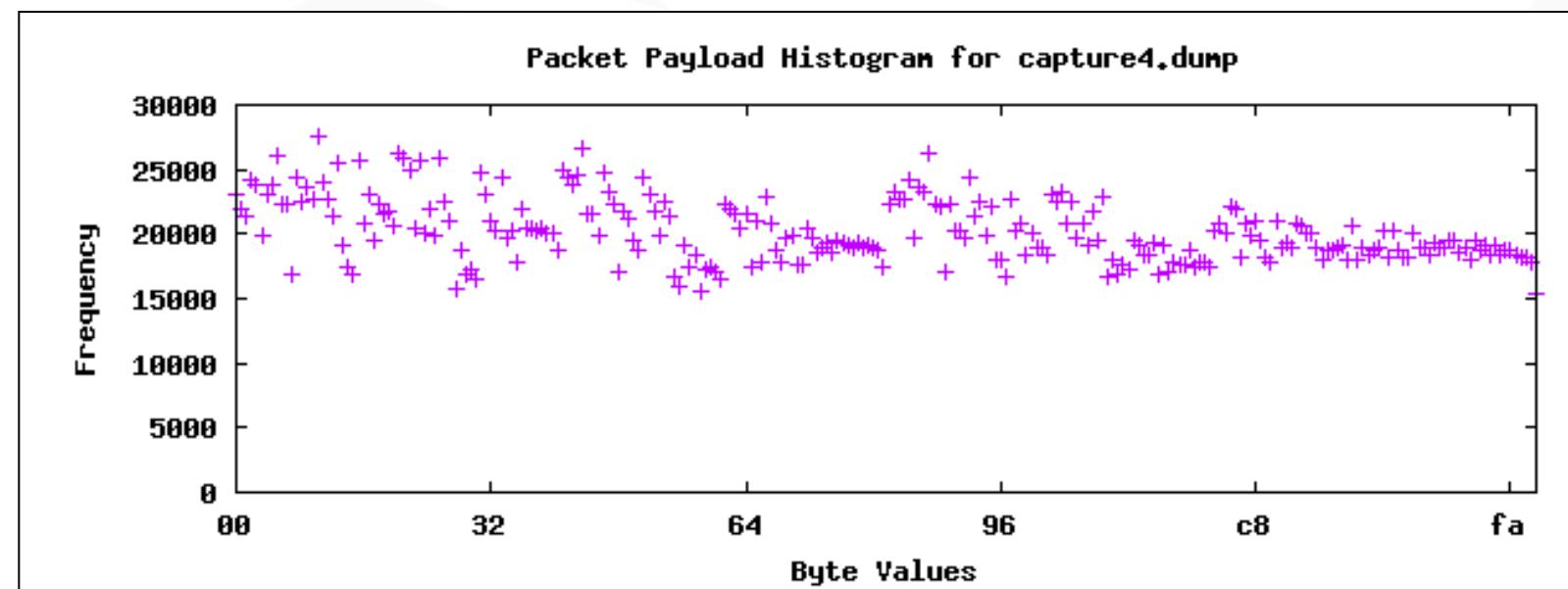
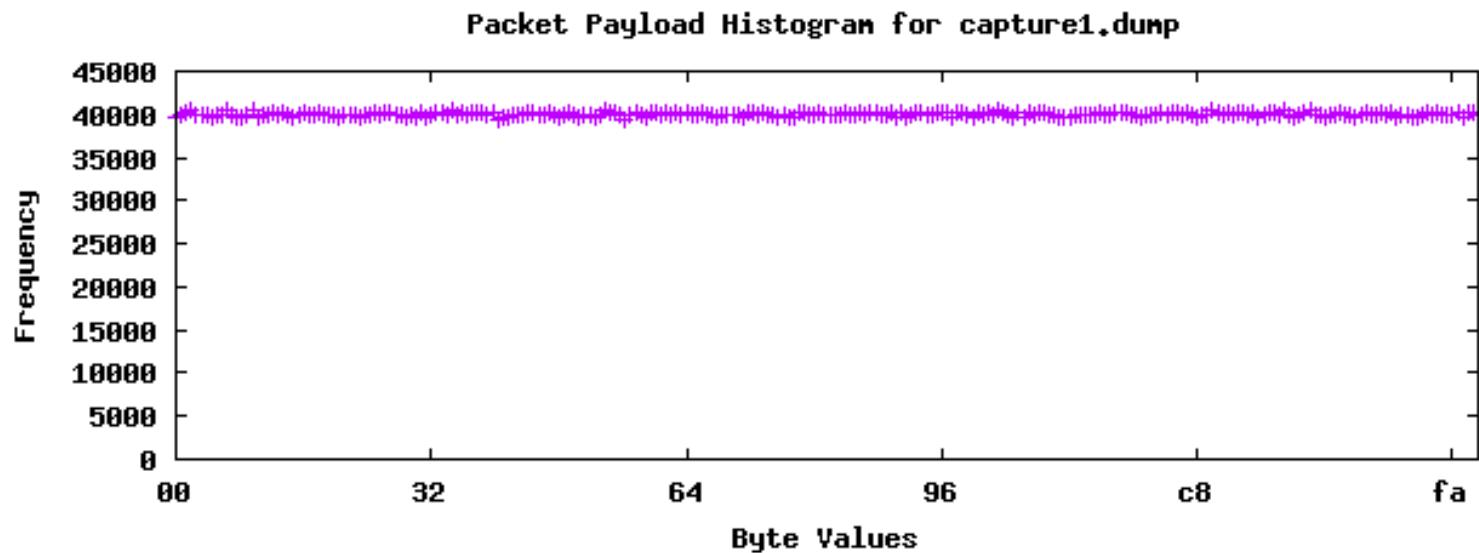
b0032001, (1, 0, 234881040, 'data processing immediate shift', 8192, 0)
    539c: \x01\x20\x03\xb0           and    r2      r3      r1  LSL  #0x0

bdf0, (1, 16, 234881040, 'data processing register shift', 8192, 2)
    53a0: \xf0\xbd\x00\x00           and    r11/FP  r0      r0  RDR  r13/SP

4bc74, (1, 16, 234881040, 'data processing register shift', 8192, 2)
    53a4: \x74\xbc\x04\x00           and    r11/FP  r4      r4  RDR  r12/IP

```

# Task: Cryptographic Analysis



# Insecure Block Cipher Modes

- AES ciphers using CTR mode effectively become a stream cipher
- Without key derivation and rotation, IV collisions compromise integrity of cipher
- The following example show a failure to use new IV

```
C:\>type ivcoltest.py
#!/usr/bin/env python
knownplain = "\xaa\xaa\x03\x00\x00\x00\x08\x00\x45\x00\x01\x48\x00\x01\x00\x00"
knowncip = "\x31\xb9\x84\x81\xe1\x96\x6e\x71\xd8\xa3\x39\x0c\xfb\x48\xaa\x61"
unknowncip = "\x31\xb9\x84\x81\xe1\x96\x6e\x71\xd8\xa3\x3d\x0c\xfb\xb5\xaa\x61"
print "Decrypted packet: "
for i in range(0,len(knownplain)):
    print "%02x"%( (ord(knownplain[i]) ^ ord(knowncip[i])) ^ ord(unknowncip[i]) ),
print("\n")

C:\>python ivcoltest.py
Decrypted packet:
aa aa 03 00 00 00 08 00 45 00 05 48 00 fc 00 00
```

# Conclusion

- Critical Infrastructure needs more testing!
  - Testing needs to be constant and remain agile
  - Testing needs to go deeper and include field controlled assets
- Research is needed in all major areas of security. Apply your talents and find your niche!
- Pentesting methodology for energy sector is publically available
  - Download it at <http://www.smartgrid.epri.com/NESCOR.aspx>
  - Provides a detailed methodology for performing penetration tests from controlling servers all the way to the controlled embedded field devices
  - Methodology is directly applicable for any critical infrastructure organization and the systems they use to control critical assets
  - Black Hat's *Pentesting Smart Grid and SCADA* course based on this methodology
- Other Energy sector specific documents that may help
  - NIST: <http://csrc.nist.gov/publications/PubsNISTIRs.html#NIST-IR-7628>
  - ASAP-SG: <http://www.smartgridipedia.org/index.php/ASAP-SG>

# Contact Information

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